East Search for Ryu No. 39

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	13	difference with between with (stack stacked) with (trench trenched) with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 18:13
S2	216	planar adj capacitor and (stack stacked) adj capacitor and (trench trenched) adj capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 18:14
S3	92	planar adj capacitor same (stack stacked) adj capacitor same (trench trenched) adj capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 18:15
S4	58	planar adj capacitor with (stack stacked) adj capacitor with (trench trenched) adj capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 18:42
S5	1	"a logic circuit is"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 18:43
S6	0	"logic circuits are"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 18:47
S7	3	varactor with logic with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 18:49
S8	3	voltage adj variable adj capacitor with logic with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:07
S9	170	audio adj circuit with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:08

S10	1	S9 and memory with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:09
S11	9	varactor with dram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:29
S12	146	varactor with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:54
S13	10	varactor with memory adj cell	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:34
S14	1	storage adj varactor with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:36
S15	1	1979-G3468B.NRAN.	DERWENT	OR	ON	2004/05/06 19:34
S16	4	storage adj varactor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:37
S17	15	storage adj capacitor with varactor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:37
S18	27	voltage adj variable with capacitor with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:54
S19	13	voltage adj variable with capacitor with memory adj cell	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:54
S20	9	voltage adj variable adj capacitor with memory adj cell	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/06 19:54